Visual Processing with Loihi 2

Intel Neuromorphic Computing Lab Andreas Wild and Yulia Sandamirskaya



June 19, 2023, CVPR 2023 Workshop on Event-based Vision

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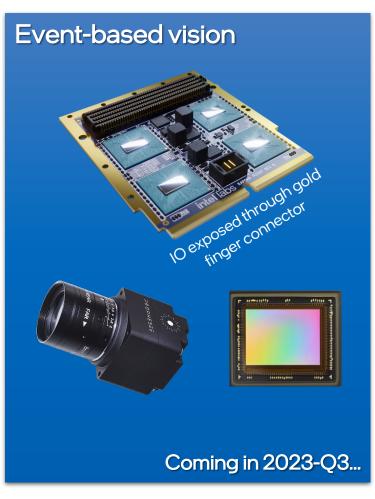
Loihi - Pioneering a new class of computer architecture to deliver orders of magnitude gains in energy and speed



- Minimal data-movement via compute/memory integration
- Massively parallel
- Event-driven computation & communication



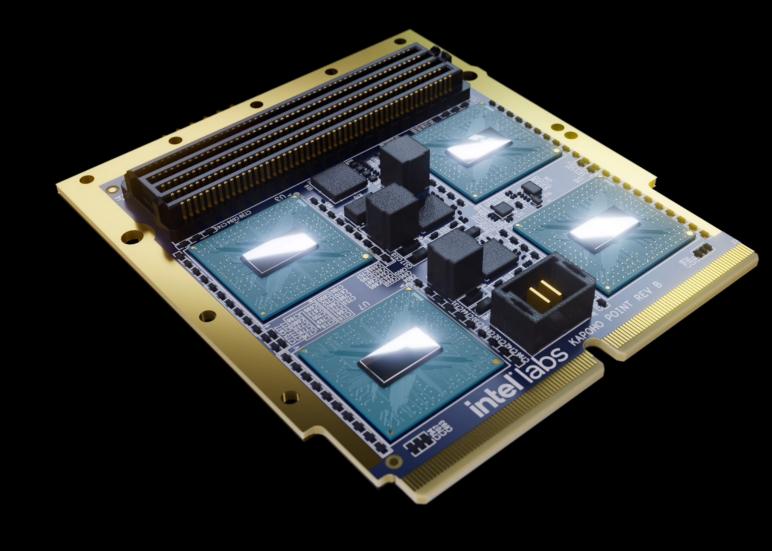
Clouc



SW Products				
Applications, Products, Services				
DL Optim	VSA			
Algorithm libraries				
Software Framework github.com/lava-nc	Event-based Multi-Paradigm Multi-Abstraction Multi-Platform Open-source			
API				
Compiler				
Runtime				
Heterogenous hardware interface				
CPU Loihi	GPU			

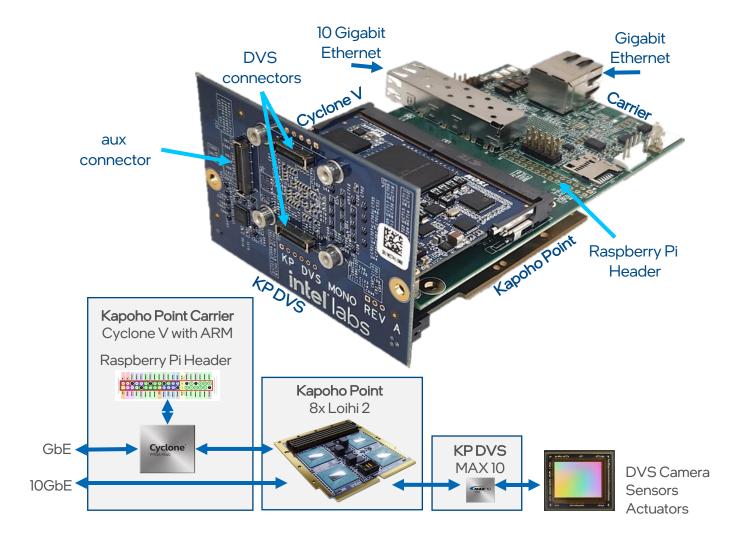
Edae

Kapoho Point Stackable 8-chip board

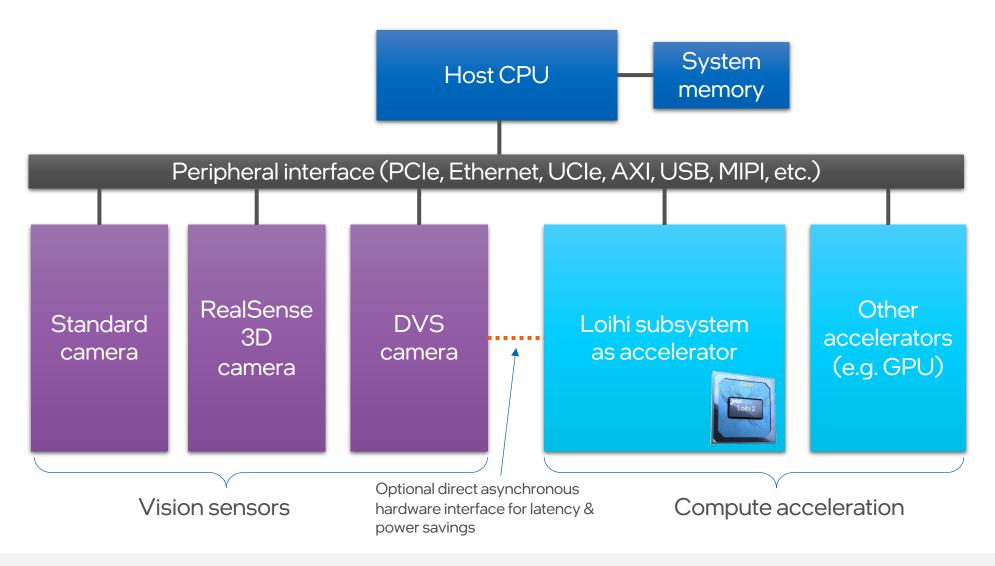


Kapoho Point Spec per Board			
Number of chips	8		
Max neurons	8.1M		
Max synapses	960 M		
Interfaces	GbE via host board 10 GbE direct to Loihi MIPI, GPIO, AER, SHS via interface board		
Dimensions	79 mm x 69 mm x 15 mm		
Weight	108g		
Power supply	12 V		

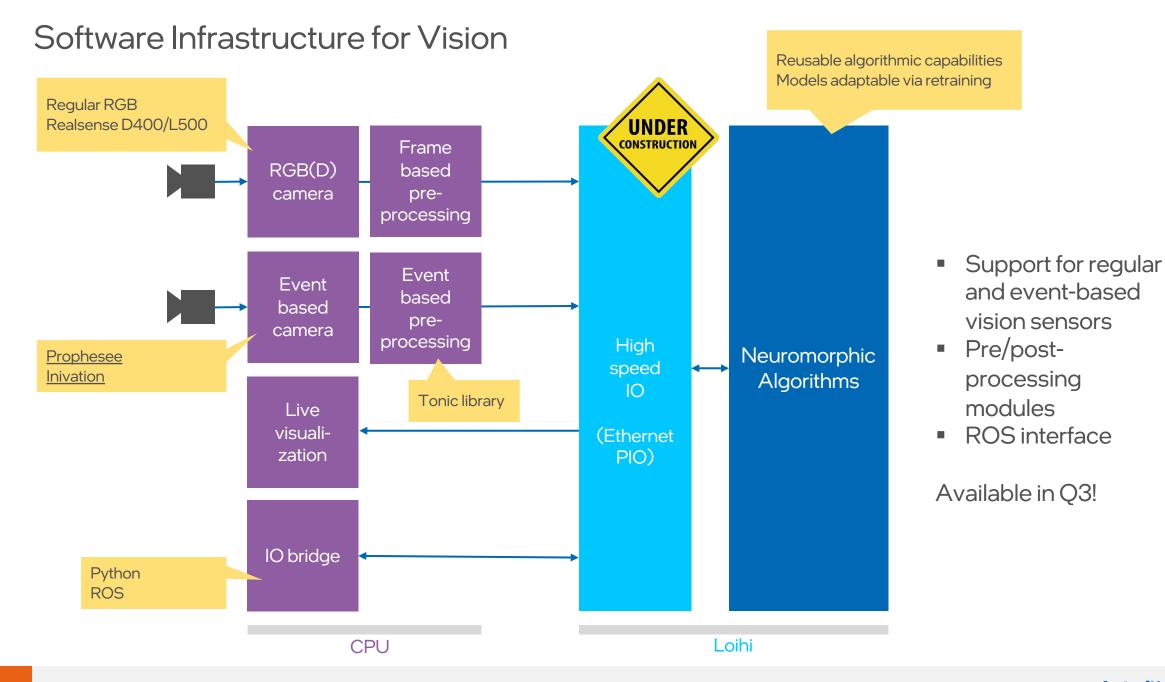
System Architecture



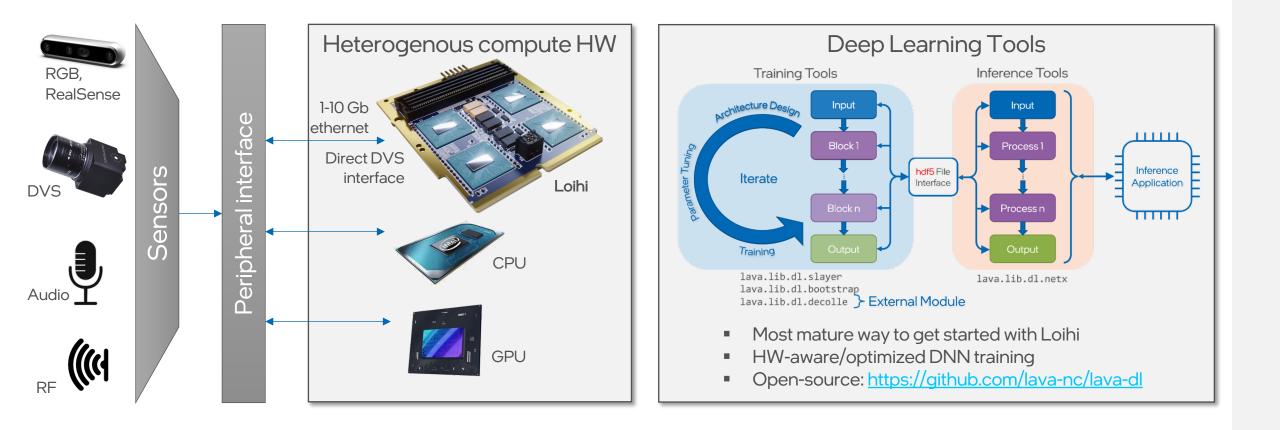
System Architecture



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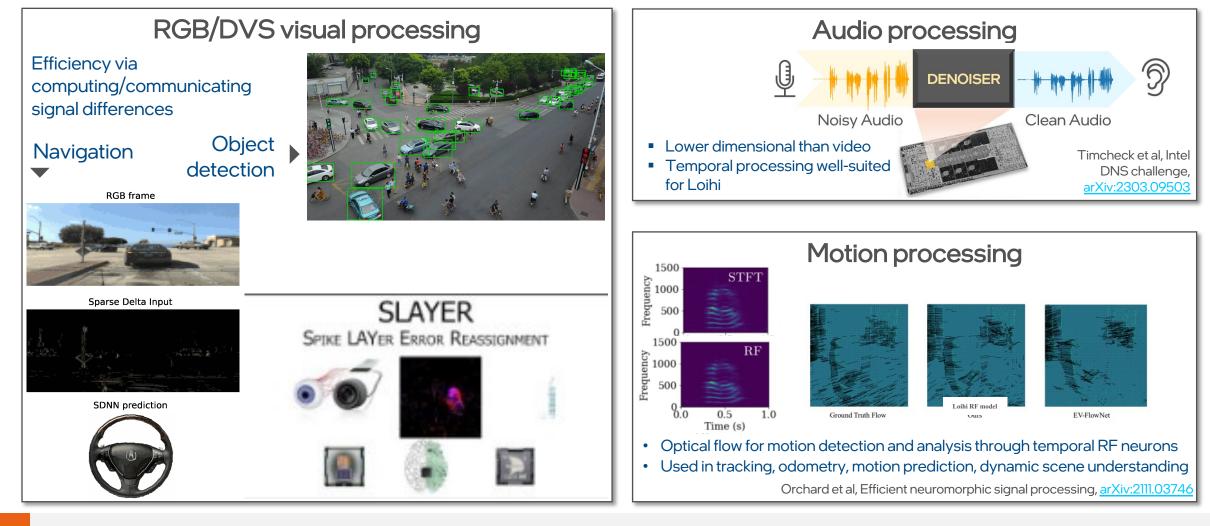


Training Vision Models with Lava-DL



Today's solutions – Conventional DNN architectures

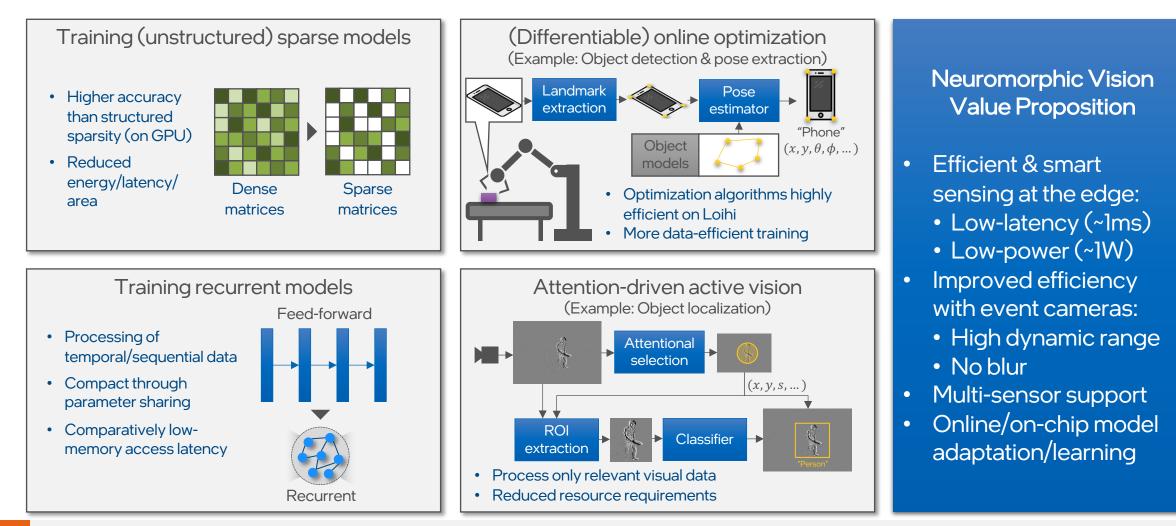
Applied to small-scale, energy-constrained, real-time, temporal problems



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Next steps – Brain-inspired approaches

Leverage Loihi's architectural differentiators for solving big/hard problems efficiently



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Application Development

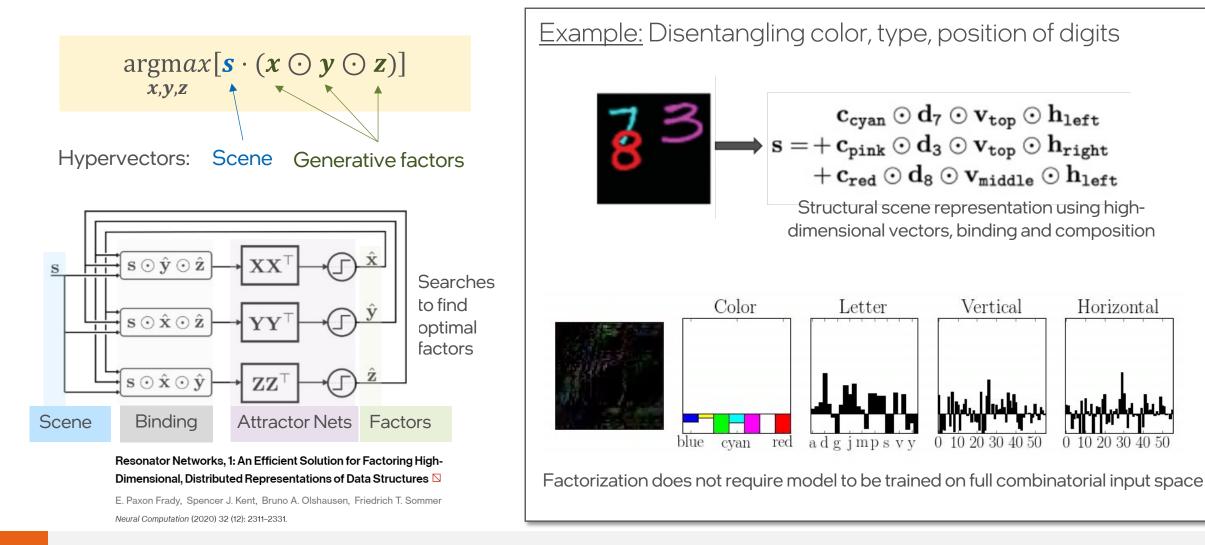
Optional: Could also end on lava-dl



Message idea

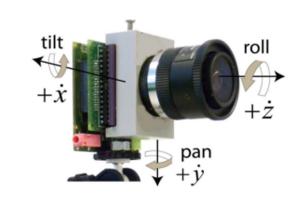
- Loihi is fast and highly energy efficient but comes at expense of capacity without external memory
- Today's focus has been mostly on smaller models where energy-efficiency matters
- Tomorrow's focus will be more on novel (bio-inspired) approaches to solve big/hard problems with fewer resources exploiting Loihi's versatile, programmable nature

Hyperdimensional (VSA-based) optimization for visual understanding



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Applied to Visual Odometry





57

Learned map

Problem:

Determine pose of moving camera given input scene of scattered shapes

Publication	Name	angle error (°) random split	angle error (°) "novel split"
[69] Kendall et al. (2015)	PoseNet	7.39	12.5
[70] Kendall et al. (2016)	Bayesian PoseNet	9.56	12.1
[74] Laskar et al. (2017)	Pairwise-CNN	6.33	10.4
[72] Walch et al. (2017)	LSTM-Pose	4.44	7.6
[77] Nguyen et al. (2019)	SP-LSTM	$2.26 - 2.95^*$	5
[73] Reinbacher et al. (2017)	Panoramic	-	5*
Renner et al. (2022) (ours)	NEVO	-	3

Dataset: Mueggler, E., Rebecq, H., Gallego, G., Delbruck, T. & Scaramuzza, D. The eventcamera dataset and simulator: Event-based data for pose estimation, visual odometry, and slam. *The International Journal of Robotics Research.*

Input

5

ormed map

n

DNN solutions, trained on dataset 1-100M parameters

Learned map

VSA Resonator solution. No need for DNN training. ~12 free parameters.

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Value proposition



- High dynamic range
- High speed
- Event-sparsity

Loihi processor

- Efficiency through...
 - minimal data movement
 - event-based communication/ computation
 - optimization for sparsity
- Support online learning

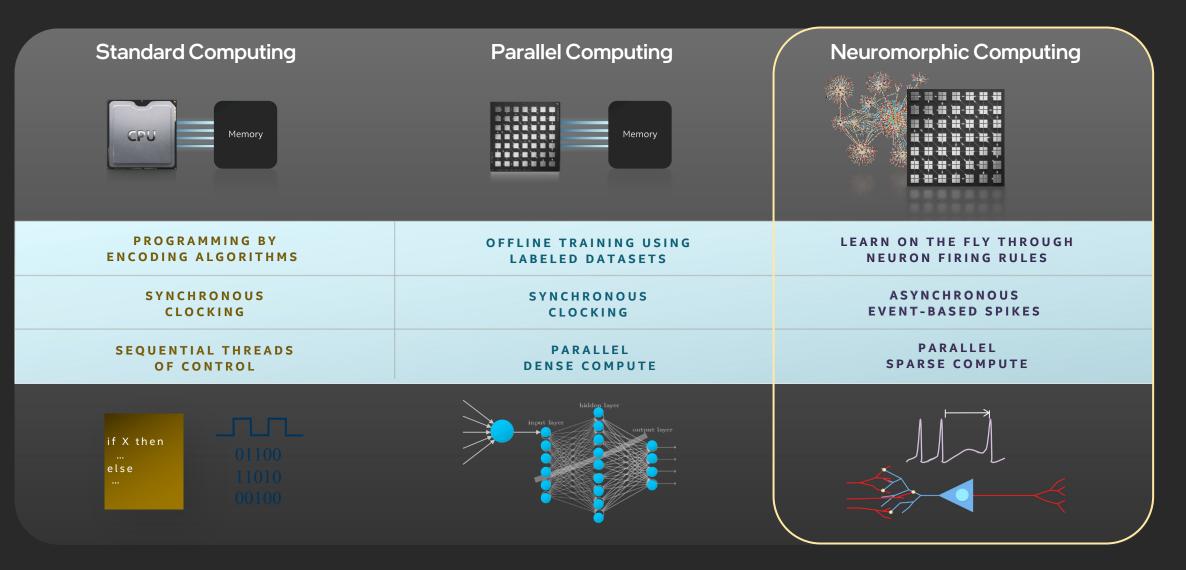
Neuromorphic algorithms

- Emphasize sparse processing
- Exploit temporal redundancy
- Extract spatio-temporal correlations
- Optimized to data and HW

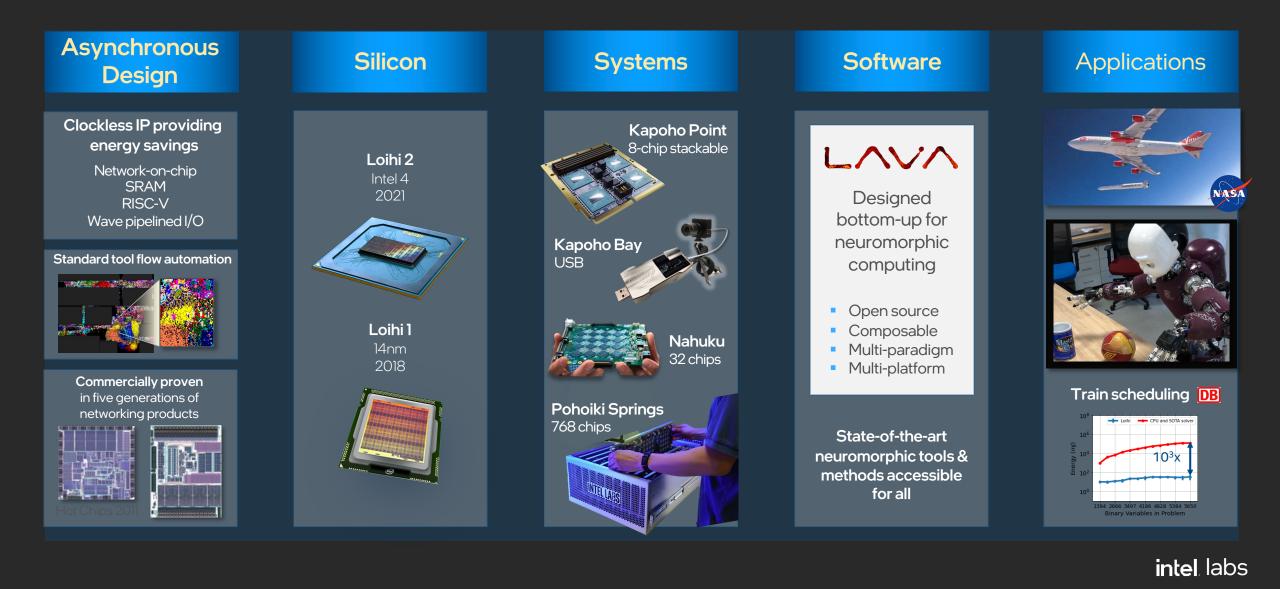
Application scope

Vision problems with continuous input High speed, varying lighting conditions Size/weight/power constrained

Leads us to a new class of computer architecture



Intel's neuromorphic research scope



Conventional vision (Ex: Navigation)

- Uses familiar CNN/DNN topologies
- Efficient frame-based vision via Σ/Δ coding
- Leverages temporal continuity in video
- Typical SynOp reduction >10x via sparse communication

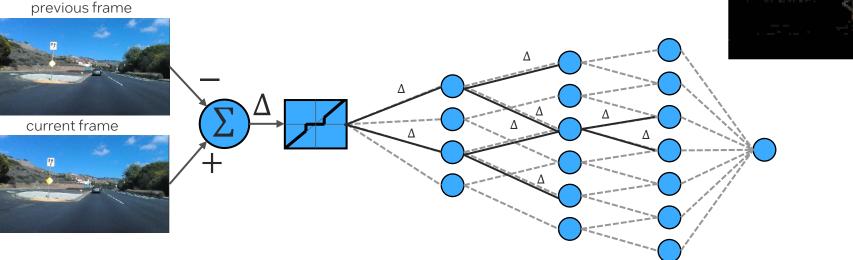
RGB frame



Sparse Delta Input



SDNN prediction



tim

Event-based vision (Ex: Gesture recognition)

- Efficient event-based vision starting at sensor
- Extremely low μs to s latency

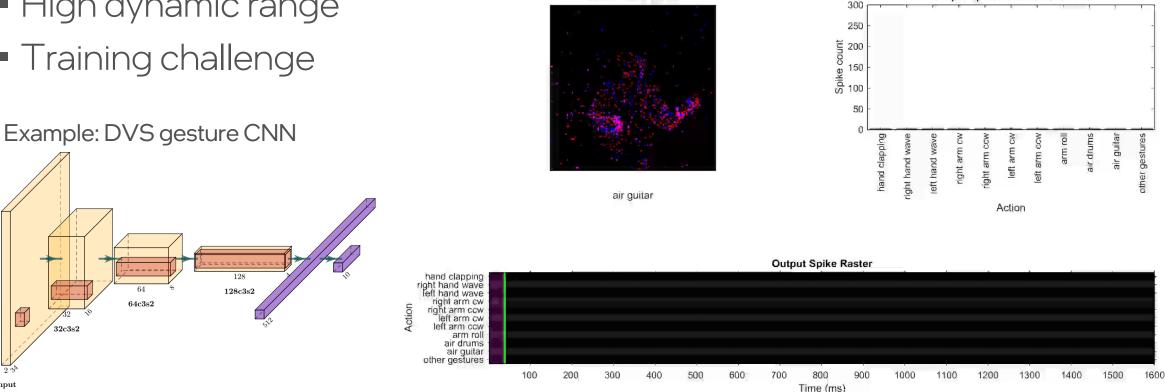
128

128c3s2

- High dynamic range
- Training challenge

64

64c3s2

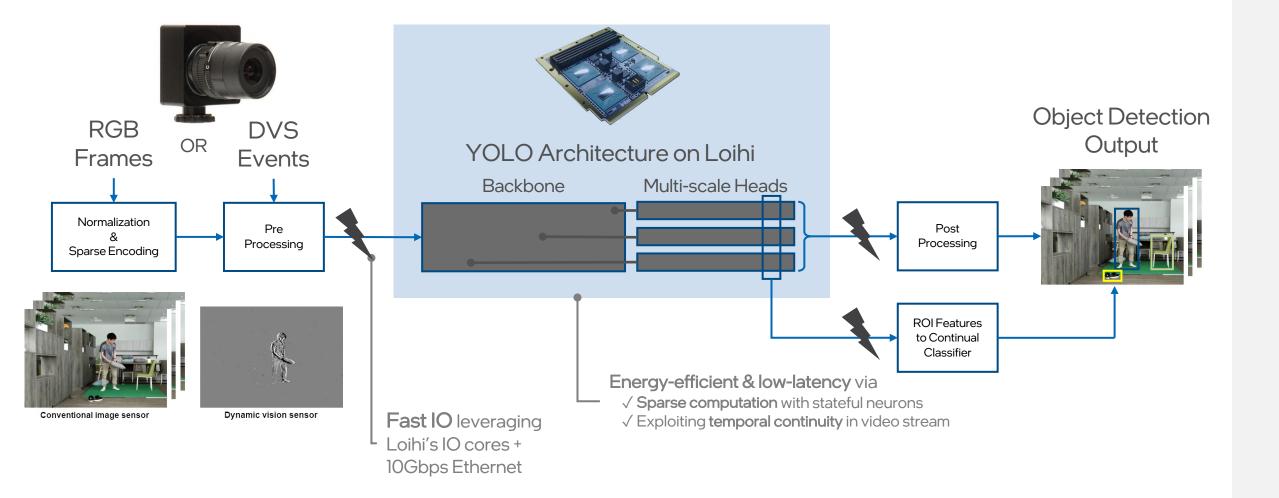


DVSGesture Video

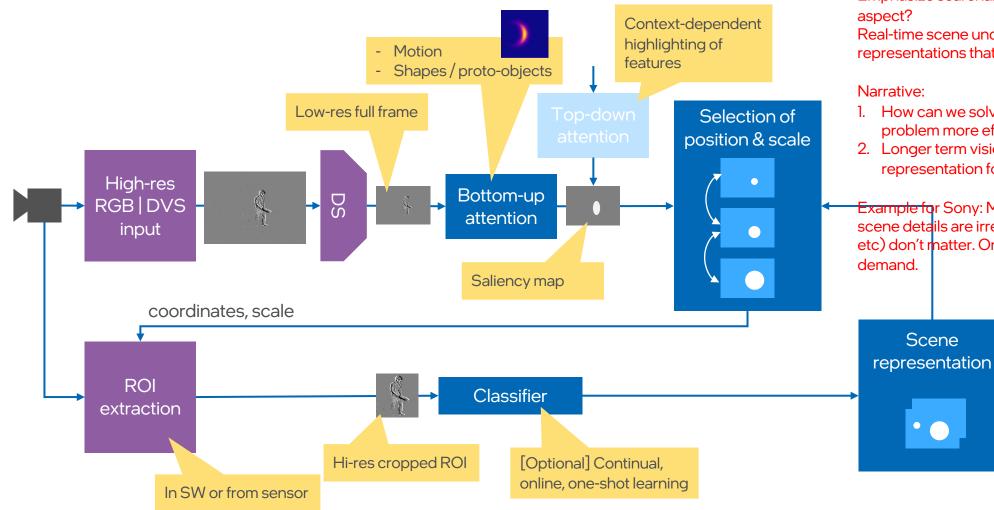
input

Output spike count distribution

Object detection via YOLO architecture



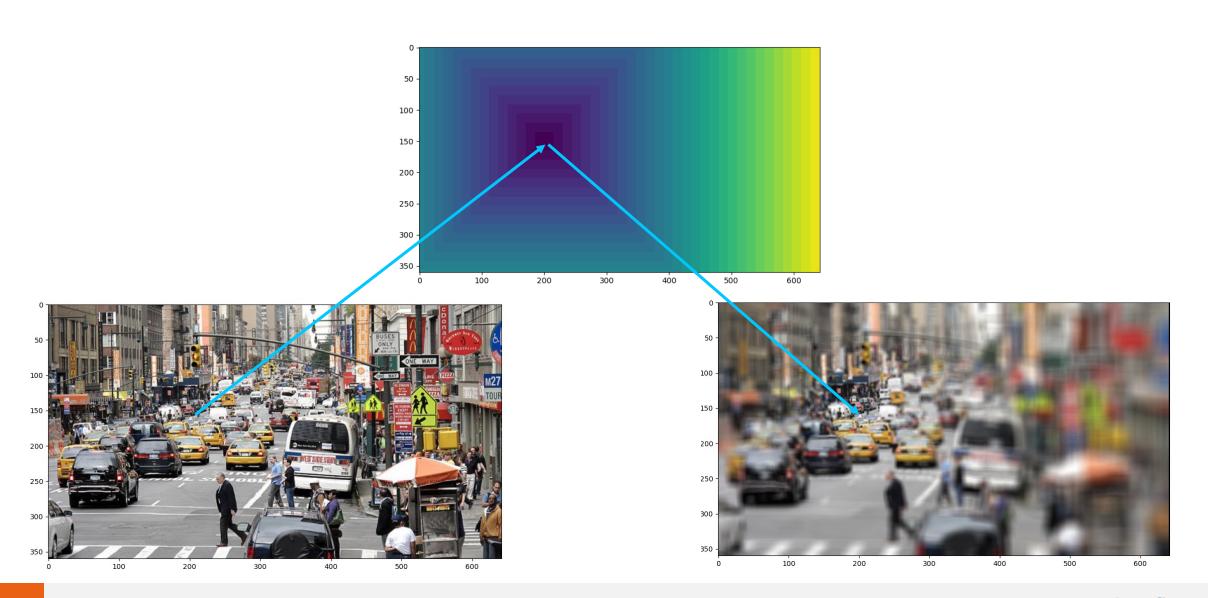
Object detection via feedback-driven attention



Emphasize searchable scene representation Real-time scene understanding. Scene representations that can be queried.

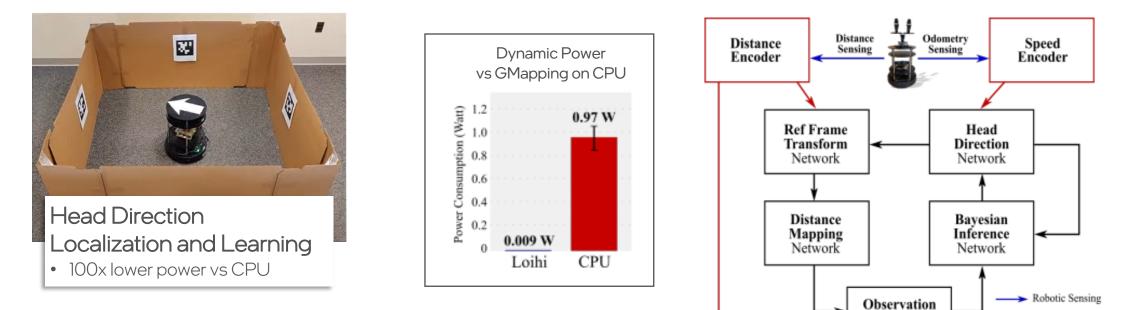
- 1. How can we solve same object detection problem more efficiently but with this arch?
- 2. Longer term vision: Real-time scene representation formation (queryable, ...)

Example for Sony: Most of the time lots of scene details are irrelevant (car color, model, etc) don't matter. Only extract this info on



SLAM-like capabilities

Rutgers (Konstantinos Michmizos)



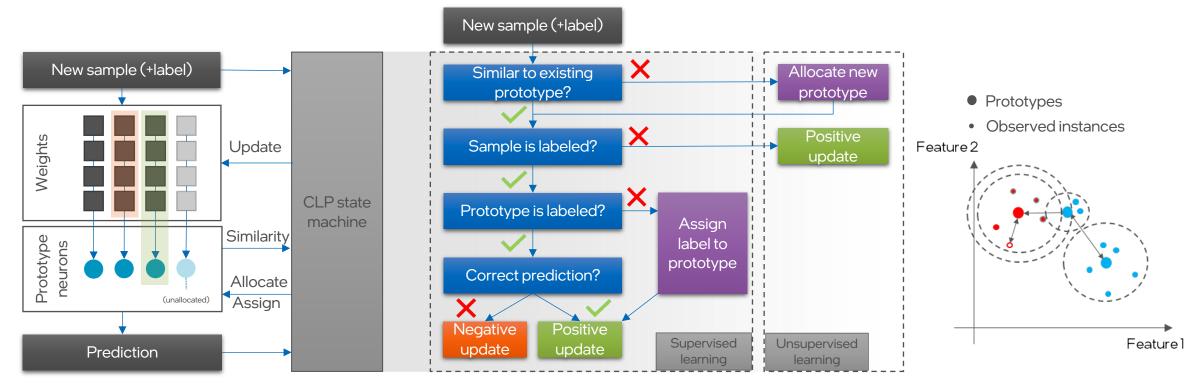
Tang, Shah, Michmizos. "Spiking Neural Network on Neuromorphic Hardware for Energy-Efficient Unidimensional SLAM," in Proceedings of the IEEE/RSJ International Conference on Intelligent Robots and Systems (IROS), Macau, China, Nov 4-8, 2019 Encoder Spikes

Synapses

Likelihood

Network

Continual Learning Prototype Classifier



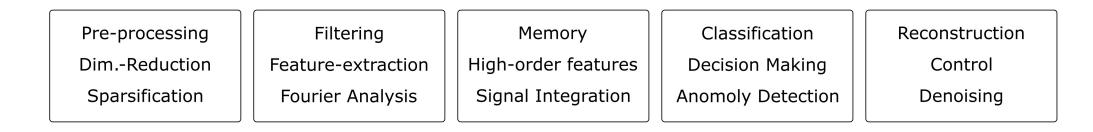
Characteristics:

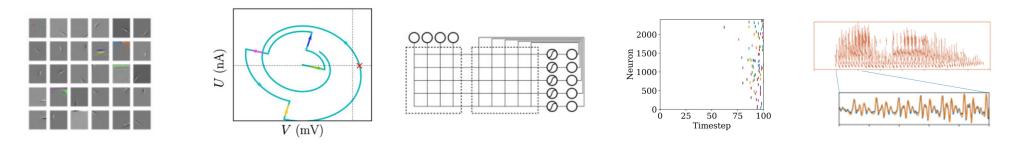
- Single-layer, local updates
- Interpretable
- Adjustable memory capacity
- Performant & energy efficient

Capabilities:

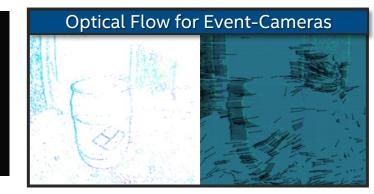
- Novelty detection
- One-shot learning
- Continual online learning
- Open-set recognition

Upcoming signal processing library

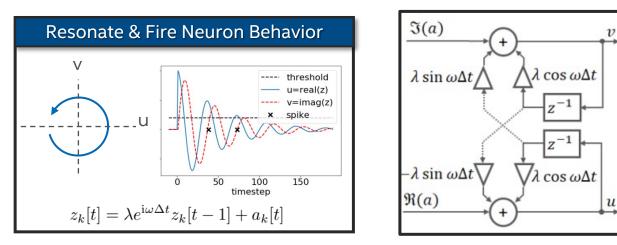




Optical flow estimation with resonate-and-fire neurons



Resonate and Fire neurons compute optical flow for event-cameras with higher accuracy and 90x fewer ops than leading DNN solution



E. P. Frady et al, "Efficient Neuromorphic Signal Processing with Resonator Neurons." Journal of Signal Processing Systems, 2022.

Average Endpoint Error on MVSEC

Indoor

AEE

1.72

1.28

1.22

Flying 2

% outlier

15.1

5.83

5.42

Indoor

AEE

1.53

1.04

0.97

Flying 3

% outlier

11.9

2.88

2.65

Indoor

AEE

1.03

0.91

0.83

EV-FlowNet2R

OURSDENSE

OurSSPIKES

Flying 1

% outlier

2.2

0.35

0.68

Latest Tools: Loihi 2 and Lava



* specs and configuration details can be found at <u>intel.com/neuromorphic</u>

- Up to 10x faster processing capability*
- Up to 60x more inter-chip bandwidth*
- Up to 1 million neurons with 15x greater resource density*
- Programmable neurons
- Graded spikes
- 3D scalable
- 10G Ethernet I/F to host

Event-based communication

Multi-Paradigm

Multi-Abstraction

Multi-Platform

Open-Source and Community-Driven

https://github.com/lava-nc

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Loihi 2 32 mm² in Intel 4

