# Event Computer Vision 10 years Assessment: Where We Came From, Where We Are and Where We Are Heading To

#### R.B. Benosman

University of Pittsburgh, Carnegie Mellon, Sorbonne Universitas

Eye& Ear and McGowan Institute

3025 E Carson st,

Pittsburgh, PA 15203





## Where we came from...

# A Chance to Move Perception from Engineering to Basic Science!





application



- Develop new bidirectional methodology to understand the brain
- Merging Computational and Biological Vision
- Importance of applications in Brain-Machine Interfaces

## Where we came from...

The retina: "the most approachable portion of the Brain"









## Where we came from...

#### **Event-based Cameras: A Long Incremental Quest**



Kwabena Boahen

5





Jorg Kramer





h chtsteiner

Christoph Posch Sorbonne Université







Daniel Matolin



2004

2008

### Where we are ...

#### **Event-Based Cameras V1.0: Panorama**



ROPHESE

• Event-based cameras have become <u>a commodity</u>



- Information is sent when it happens
- When nothing happens, nothing is sent or processed
- Sparse information coding
- Time is the most valuable information

### **Properties Event Based sensors?**



• Amount of Data generated from a moving car per 10 miliseconds



Time (milliseconds)

Dataset	PokerDVS			N-MNIST			DvsGesture			NavGesture-walk		
(Mean Event Rate	170.4 ev/ms			13.6 ev/ms			56.9 ev/ms			188.6 ev/ms		
& Sensor Size)	35x35			28x28			128x128			304x240		
1. Time Window (ms)	1	10	100	1	10	100	1	10	100	1	10	100
2. Mean Number of events in TW	101	390	486	22	84	229	53	340	1751	285	2818	13279
(percentage of active pixels)	(8%)	(32%)	(40%)	(3%)	(11%)	(29%)	(<1%)	(2%)	(11%)	(<1%)	(4%)	(18%)
3. Max Number of events in TW	356	848	1052	223	312	597	467	2056	9191	2599	18296	68128
(percentage of active pixels)	(29%)	(69%)	(86%)	(28%)	(40%)	(76%)	(3%)	(13%)	(56%)	(4%)	(25%)	(93%)
4. Working Memory Size (kB)	0.8	2.1	2.0	0.2	0.7	1.0	0.4	27	14.0	2.2	22.5	106.2
Dynamic - Average case	0.8	5.1	5.9	0.2	0.7	1.0	0.4	2.7	14.0	2.3	22.5	100.2
5. Working Memory Size (kB)	2.8	6.8	8.4	1.8	25	18	37	16.4	73.5	20.8	146.3	545.0
Dynamic - Worst case	2.0	0.0	0.4	1.0	2.5	4.0	5.7	10.4	13.5	20.0	140.5	545.0
6. Allocated Memory Size (kB)	9.8	9.8	9.8	6.3	6.3	6.3	131	131	131	584	584	584
7. Memory ratio dynamic/static	80%	370%	10%	30%	110%	20%	10%	20%	110%	10%	10%	180%
(Average Case)	070	5270	4070	570	1170	2970	1 70	270	1170	1 70	470	1070
8. Memory ratio dynamic/static	29%	69%	86%	28%	40%	76%	3%	13%	56%	4%	25%	93%
(Worst Case)	2,70	0,770	0070	2070	1070	, 570	5.0	1570	5070	1,0	2570	15.10

## Computer vision: the impossible trade off of power vs frame rate



## High Power & High Latency

#### **Origins of Imaging**

illum in tabula per radios Solis , quâm in cœlo contingit : hoc eft;fi in cœlo fuperior pars deliquiñ patiatur,in radiis apparebit inferior deficere,vt ratio exigit optica.



Sie nos exacté Anno .1544. Louanii eclipfim Solis obferuauimus, inuenimusé; deficere pauló plus § dex-













#### **Event Computation**

VS



**Batch or Frame** 



Compute the new 1 pixelchange frame and compute result using <u>the whole</u> <u>frame</u>

# Incremental Event Vision: everything can be written as an

incremental event process



Event-Based Solution to the PnP Problem



Event-based Face Detection in the Blink of an Eye

# Where we are heading to

#### **Replace arbitrers**

- Current locks are caused by the arbitrer that scrambles event times and makes computation difficult
- Event Cameras are becoming compressed Imagers.





# Where we are heading to: the promise

Moore's Law: The number of transistors on microchips doubles every two years Our World in Data Moore's law describes the empirical regularity that the number of transistors on integrated circuits doubles approximately every two years. This advancement is important for other aspects of technological progress in computing - such as processing speed or the price of computer Transistor count 50,000,000,000 10.000.000.000 5.000.000.000 1,000,000,000 500.000.000 100.000.000 50,000,000 10.000.000 5,000,000 1,000,000 500,000 100,000 ARM 50,000 5.000 1.000 Year in which the microchip was first introduced Data source: Wikipedia (wikipedia.org/wiki/Transistor\_count) OurWorldinData.org - Research and data to make progress against the world's largest problems. Licensed under CC-BY by the authors Hannah Ritchie and Max Roser



Von Neumann bottleneck



Promise of a new path solving for:

Plateau in current main stream computing technologies

Low Power and Latency

# Where we are heading to

Existing Neuromorphic Processing Hardware is based on what we know of the brain



Existing hardware is based on the concept of replicating biological neurons into silicon
Limited use cases!

# Where we are heading to

Replicating nature's solutions is not always the optimal path to solve an engineering problem.



Understanding rather than replicating

There is a need to find the right level of abstraction

## Where We Are Heading To

• We should explore new forms of events' acquisition



- Find a better link between images and events and see how to connect with decades of CV without losing the advantages of events
- We need a dedicated processor adapted to the temporal precision and sparseness of data of events and the amount of generated data
- Event cameras are the future if we explore their temporal properties
- New kind of engineers that undertstand neurosciences where « biological » events are studied